

Impact of Emitter Electron Launcher on the Performance of Type-II InP/GaAsSb DHBTs

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Abstract—This work investigates the impact of an emitter electron launcher on the DC and RF performance of Type-II InP/GaAsSb double heterojunction bipolar transistors (DHBTs). The introduction of the electron launcher results in improved DC characteristics, including an improved base current ideality factor and an increased common-emitter current gain, β . The improved ideality factor indicates suppressed recombination current, leading to enhanced electron injection efficiency into the base. Statistical analysis further confirms reduced emitter-size dependence and improved β uniformity, with an overall increase of approximately 30% compared to a reference standard DHBT. RF measurements show no significant degradation, with only a slight reduction in f_T , particularly for narrow devices, attributed to increased emitter resistance due to higher Ga content. These results demonstrate that the emitter electron launcher is an effective approach for improving DC performance while maintaining RF characteristics, offering potential for further device scaling and optimization.

Keywords—Double heterojunction bipolar transistors (DHBTs), modified double heterojunction bipolar transistors (mDHBTs), GaAsSb, InP, Breakdown, THz.

I. INTRODUCTION

Double heterojunction bipolar transistors (DHBTs) based on the Type-II InP/GaAsSb material system have attracted significant attention for high-speed and high-frequency applications due to their favorable band alignment and excellent transport properties [1]. In particular, these devices offer high breakdown voltage and competitive RF performance, making them strong candidates for next-generation millimeter-wave and terahertz technologies [2]. However, further performance improvements are often limited by recombination currents in the base-emitter junction, which degrade the base current ideality factor and reduce the common-emitter current gain, β .

Various approaches have been proposed to enhance carrier injection efficiency and suppress recombination mechanisms in DHBTs. Among these, the incorporation of an emitter electron launcher has emerged as a promising technique to improve electron injection into the base by engineering the emitter energy profile. By modifying the emitter composition, particularly through increased Ga content, the electron launcher can effectively reduce the recombination component of the base current, leading to improved DC performance. Increasing the Ga content of the GaInP composition results in

a larger ΔE_C [3]. This can be increased in the GaInP emitter to further accelerate electrons by creating a larger energy ramp at the emitter–base junction. However, this is limited by lattice mismatch and strain between GaInP and GaAsSb.

Despite these advantages, the impact of such design modifications on RF performance remains a critical concern. Changes in emitter composition may introduce increased emitter resistance, which can potentially degrade high-frequency figures-of-merit such as the cutoff frequency, f_T , and the maximum oscillation frequency, f_{MAX} . Therefore, a comprehensive evaluation of both DC and RF characteristics is essential to assess the overall benefit of the emitter electron launcher concept.

In this work, the effect of an emitter electron launcher on the performance of Type-II InP/GaAsSb DHBTs is investigated. Devices with identical geometries are compared to a reference structure to isolate the influence of the emitter design. Both DC and RF measurements, along with statistical analysis across device sizes, are presented to evaluate improvements in gain, ideality factor, and scalability. The results demonstrate enhanced DC performance with minimal impact on RF characteristics, highlighting the potential of the emitter electron launcher for future high-speed device optimization.

II. DEVICE FABRICATION

DHBT epitaxial layers differing only in emitter composition were grown by metal-organic chemical vapor deposition (MOCVD) on 2-inch semi-insulating InP substrates. The details of the epilayers are given in Table 1. DHBTs with the $\text{Ga}_{0.22}\text{In}_{0.78}\text{P}/\text{GaAs}_{0.59}\text{Sb}_{0.41}$ (DHBT1) and $\text{Ga}_{0.27}\text{In}_{0.73}\text{P}/\text{GaAs}_{0.59}\text{Sb}_{0.41}$ (DHBT2) emitter/base interface layers were successfully fabricated. The two structures differ in the emitter composition adjacent to the base layer: $\text{Ga}_{0.22}\text{In}_{0.78}\text{P}/\text{GaAs}_{0.59}\text{Sb}_{0.41}$ (DHBT1) and $\text{Ga}_{0.27}\text{In}_{0.73}\text{P}/\text{GaAs}_{0.59}\text{Sb}_{0.41}$ (DHBT2). In both cases, the emitter consists of 15 nm $\text{Ga}_y\text{In}_{1-y}\text{P}$, where the first 5 nm adjacent to the base have a constant composition, followed by a 10 nm region that is linearly graded to $y=0$. The intermediate n^+ InP layer and the GaInAs cap are doped at concentrations of $\sim 1.5 \times 10^{19} \text{ cm}^{-3}$ and $\sim 3.8 \times 10^{19} \text{ cm}^{-3}$, respectively. The transistors feature a 20-nm-thick $\text{GaAs}_x\text{Sb}_{1-x}$ base with a

Table 1. Epitaxial Layer Structure

	Material	Doping [cm^{-3}]	Thickness [nm]
Emitter Contact	$\text{Ga}_{0.25}\text{In}_{0.75}\text{As}$	Si: $3.8\text{E}19$	5
	$\text{Ga}_{0.47}\text{In}_{0.53}\text{As} \rightarrow$	Si: $3.8\text{E}19$	10
	$\text{Ga}_{0.25}\text{In}_{0.75}\text{As}$		
	$\text{Ga}_{0.47}\text{In}_{0.53}\text{As}$	Si: $3.8\text{E}19$	20
Emitter (DHBT1)	InP	Si: $1.5\text{E}19$	130
	InP	Si: $2.2\text{E}19$	5
	$\text{Ga}_{0.22}\text{In}_{0.78}\text{P} \rightarrow$	Si: $2.2\text{E}16$	10
	InP		
	$\text{Ga}_{0.22}\text{In}_{0.78}\text{P}$	Si: $2.2\text{E}16$	5
Emitter (DHBT2)	InP	Si: $1.5\text{E}19$	130
	InP	Si: $2.2\text{E}19$	5
	$\text{Ga}_{0.27}\text{In}_{0.73}\text{P} \rightarrow$	Si: $2.2\text{E}16$	10
	InP		
	$\text{Ga}_{0.27}\text{In}_{0.73}\text{P}$	Si: $2.2\text{E}16$	5
Base	$\text{GaAs}_{0.41}\text{Sb}_{0.59} \rightarrow$	C: $8.5\text{E}19$	20
	$\text{GaAs}_{0.59}\text{Sb}_{0.41}$		
Collector	InP	Si: $9.4\text{E}16$	125
Sub-Collector	InP	S: $3.1\text{E}19$	50
Collector Contact	$\text{Ga}_{0.47}\text{In}_{0.53}\text{As}$	Si: $3.0\text{E}19$	20
Buffer	InP	S: $2.8\text{E}19$	300
Substrate	InP	Semi-insulating	350000

sheet resistance of $R_{\text{SH}} \approx 1140 \Omega/\square$ and an average doping concentration of $N_{\text{B, AV}} \approx 8.5 \times 10^{19} \text{ cm}^{-3}$. The InP collector, buffer, and substrate are identical for both structures. The bandgap differences ΔE_{G} for 27% Ga relative to the standard 22% Ga in GaInP composition is 52 meV [4].

DHBTs were fabricated side-by-side in a self-aligned triple mesa process, with the emitter and base contacts defined by electron-beam lithography. The base contact metallization employs an electron-beam-evaporated Pt/Ir/Pt/Au refractory stack to suppress metal diffusion into the base during high-temperature benzo-cyclo-butene (BCB) curing [5]. The emitter mesa sidewalls and the extrinsic base surface were passivated with atomic layer deposited (ALD) Al_2O_3 . The self-aligned emitter and base mesas were formed by combined dry and wet etching, followed by photolithographic definition and electron-beam evaporation of the collector contact. After deposition of the collector contact, the devices were electrically isolated and planarized using BCB. Finally, the process was completed by depositing ground-signal-ground (GSG) probe pads.

III. RESULTS AND DISCUSSIONS

A. DC Measurements

The static performance of the DHBTs was characterized using an HP4156B semiconductor parameter analyzer. The Gummel characteristics measured at $V_{\text{CB}} = 0 \text{ V}$ for a $(0.25 \times 7.5) \mu\text{m}^2$ DHBT1 and DHBT2 are shown in Fig. 2. The DC results show a reduced base current ideality factor for the new emitter design (DHBT2), along with an improved gain compared to the standard DHBT (DHBT1). The base current ideality, factor η_{B} , decreases from 1.38 to 1.22. The reduction in the ideality factor indicates a decrease in recombination current. Therefore, in DHBT2, electron injection into the base is enhanced, resulting in an increased common emitter current

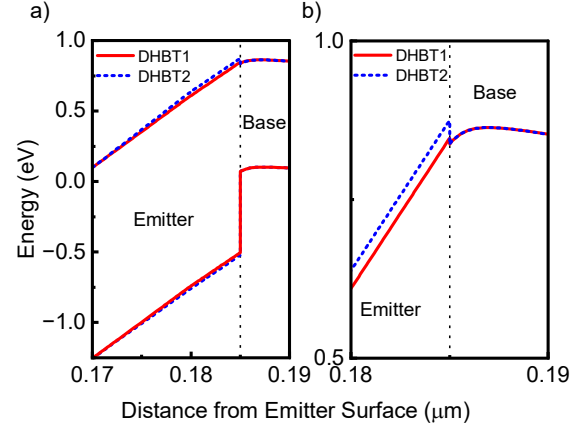


Fig. 1. a) Equilibrium band diagrams of the DHBT1 and DHBT2 at the emitter-base junction. b) Zoomed-in conduction band diagram at the emitter-base junction.

gain β from 22 to 28. The collector ideality factor remains unchanged at 1.01 for both structures.

B. Small-Signal RF Measurements

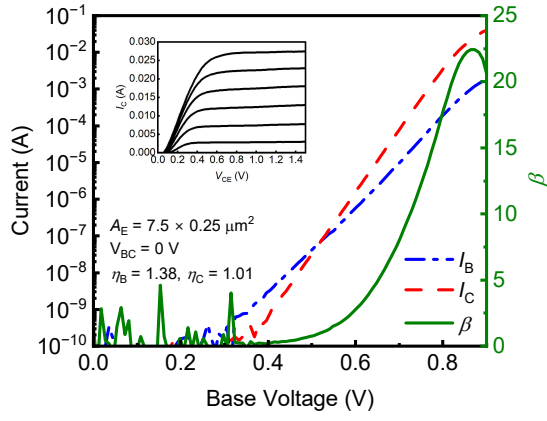
The small-signal RF performance was measured using a PNA-X vector network analyzer over the frequency range of 0.2-50 GHz with an input power of -30 dBm. An off-wafer impedance standard and LRRM (line-reflect-reflect-match) calibration were employed to establish the reference planes at the probe tips. Following conventional short-open de-embedding, Mason's unilateral power gain (U) and the common-emitter short-circuit current gain $|h_{21}|^2$ were plotted as a function of frequency, as shown in Fig. 3. Extrapolation based on single-pole fits over the full frequency range yields a peak $f_{\text{MAX}} = 745 \text{ GHz}$ with $f_{\text{T}} = 460 \text{ GHz}$ at $V_{\text{CE}} = 1.1 \text{ V}$ for the DHBT1, and a peak $f_{\text{MAX}} = 786 \text{ GHz}$ with $f_{\text{T}} = 445 \text{ GHz}$ at $V_{\text{CE}} = 1.1 \text{ V}$ for the DHBT2. The RF results show no notable difference between the two structures, with only a slight reduction in f_{T} and a slight increase in f_{MAX} for the DHBT2.

C. Statistical Analysis

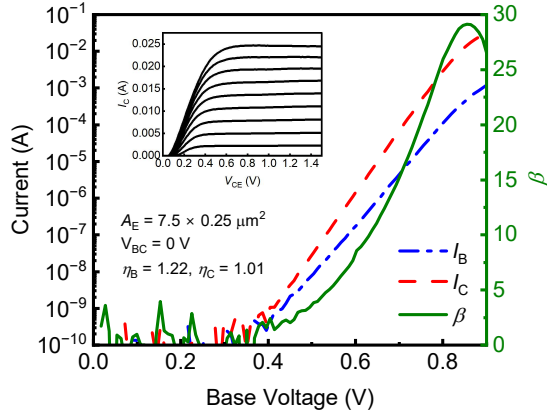
A comprehensive statistical analysis was performed on more than 100 devices from each structure, as illustrated in Fig. 4. The results indicate that the new design mitigates emitter-size effects and yields a more stable β as a function of device size. Furthermore, the overall β is about 30% higher in DHBT2. The RF results indicate a slight decrease in f_{T} for DHBT2, especially for devices with narrower emitter widths. This is attributed to the higher emitter resistance in DHBT2 due to the increased Ga content in the emitter. Nevertheless, the overall RF performance, including f_{MAX} , remains largely unaffected.

IV. CONCLUSION

In conclusion, the incorporation of an emitter electron launcher in Type-II InP/GaAsSb DHBTs improves DC



(a) DHBT1



(b) DHBT2

Fig. 2. Gummel characteristics ($V_{CB} = 0$ V) of a $0.25 \times 7.5 \mu\text{m}^2$ (a) DHBT and (b) mDHBT. The DC gain as a function of V_{BE} is also plotted. Inset: Corresponding common-emitter I - V curves.

performance while preserving RF characteristics. The new design (DHBT2) shows a reduced base current ideality factor, indicating lower recombination current and enhanced electron injection, resulting in a higher β . Statistical analysis confirms improved uniformity and reduced emitter-size dependence.

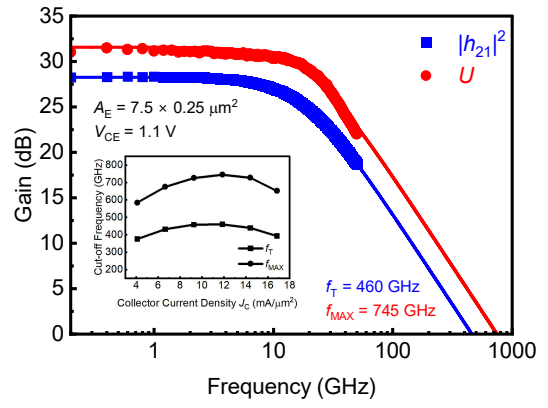
RF performance remains largely unchanged, with only a slight reduction in f_T attributed to increased emitter resistance, while f_{MAX} is unaffected. These results highlight the effectiveness of the emitter electron launcher for improving device performance and enabling further scaling.

ACKNOWLEDGMENT

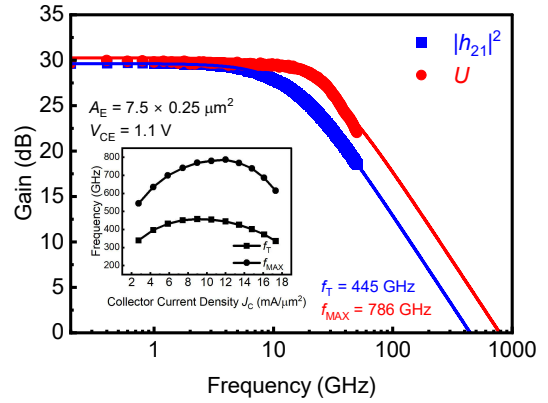
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(a) DHBT1

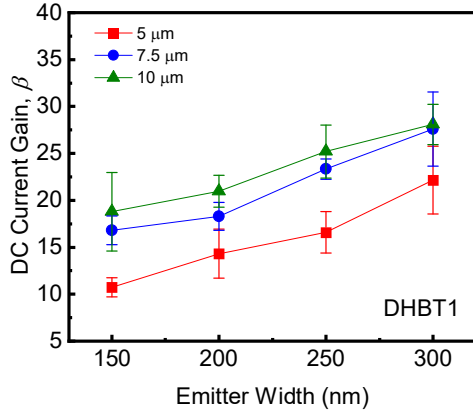


(b) DHBT2

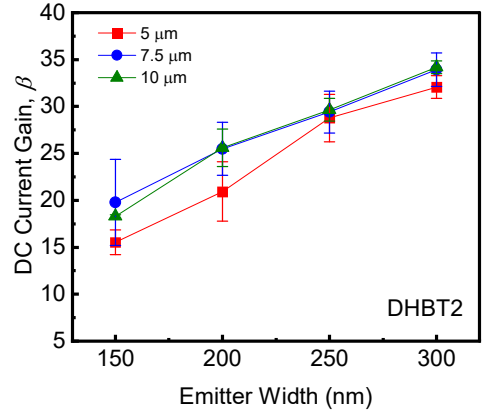
Fig. 3. Measured small-signal current gain $|h_{21}|^2$, and Mason's unilateral gain U of (a) DHBT and (b) mDHBT. f_T and f_{MAX} are determined from single-pole fits and extrapolations. Inset: Bias dependence of f_T and f_{MAX} vs. current density.

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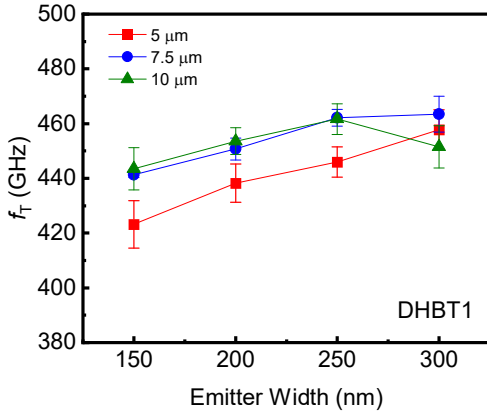
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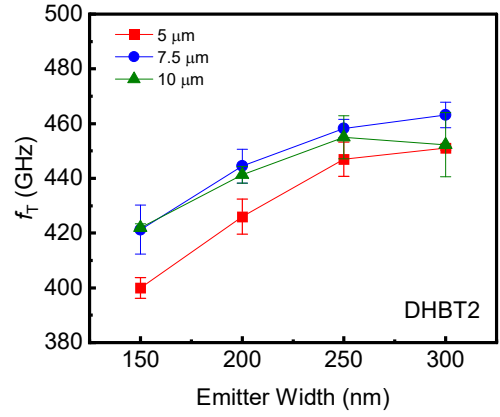
(a) β scaling behavior in DHBT1.



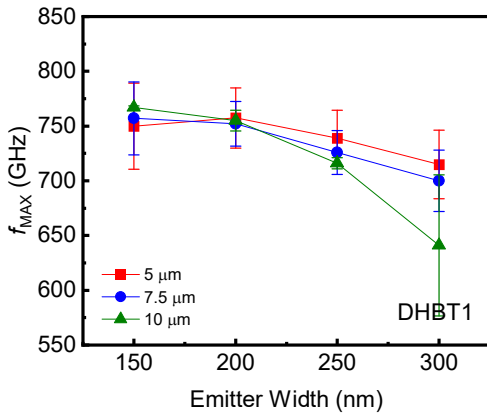
(b) β scaling behavior in DHBT2.



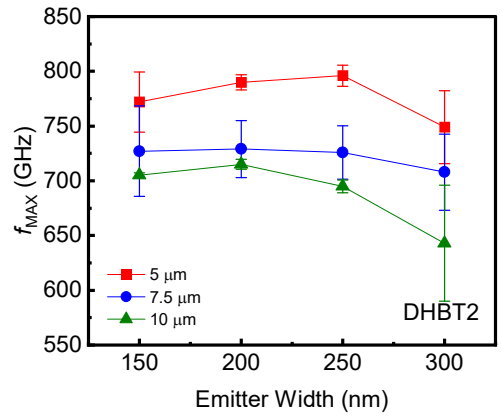
(c) f_T scaling behavior in DHBT1.



(d) f_T scaling behavior in DHBT2.



(e) f_{MAX} scaling behavior in DHBT1.



(f) f_{MAX} scaling behavior in DHBT2.

Fig. 4. Statistical analysis of DHBT1 and DHBT2.